

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**IN RE APPLICATION OF:  
YIN TAT MA**

**SERIAL NO. : 10/501,651**

**FILED: JAN. 10, 2005**

**FOR: ON-CHIP ESD PROTECTION  
CIRCUIT FOR COMPOUND  
SEMICONDUCTOR  
HETEROJUNCTION BIPOLAR  
TRANSISTOR RF CIRCUITS**

**Examiner: D.H. Patel**

**Group Art Unit: 2836**

**Confirmation No.: 5137**

**REQUEST FOR REFUND**

Mail Stop 16  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In further reply to the final Office Action mailed February 2, 2007, Applicant believes that the previously filed Requests for Extension of Time and the accompanying fees are unnecessary as Applicant's Reply (received at the PTO on April 12, 2007) to the final Office Action dated February 2, 2007 is well within the shortened statutory period. Thus, the present application would have remained pending even without the Extension of Time. Further, Applicant's Reply has placed the present application in condition of allowance as confirmed by telephone communications with Examiner Patel. Accordingly, a Request for Refund is hereby submitted.

Refund for Applicant's previously filed fees in the total amount of \$225 is requested. Among those fees, an amount of \$165 was paid on June 28, 2007 (EFS ID 1923571), and an amount of \$60 was paid on May 8, 2007 (EFS ID 1756362), both

using a credit card. Accordingly, it is requested that the same amount be credited back to said credit card, or to our deposit account #01-1960.

The Office is also authorized to charge all fees under 37 CFR 1.17, or all required extension of time fees or any deficit and credit any surplus in such fees to our deposit account #01-1960.

Please direct all correspondence to **Myers Dawes Andras & Sherman LLP**,  
19900 Mac Arthur Blvd., 11<sup>th</sup> Floor, Irvine, California 92612.

Respectfully submitted,



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